

**Amendments to the Specification:**

Please replace the paragraph beginning on page 11, line 17, with the following amended paragraph:

If the value of  $k_{wpred}$  is such that no filtering is necessary, the output of the filtering circuit consists of the original data  $w(0)$  to  $w(7)$ , the control circuit CTRL controlling, for example, a multiplexer, not shown in the diagram, making it possible to choose between the filtered data  $w_f$  and the original data  $w$ . W the original data  $w$ .